a first digital signal processor for converting analog video signals into digital video signals and for compressing the video signals into video bytes;

a second digital signal processor for decompressing received digital video bytes into digital video signals and for converting the decompressed digital video signals into analog video signals;

a third digital signal processor for converting analog audio signals into digital audio signals, for compressing the audio digital signals into audio bytes, for decompressing received audio bytes into audio digital signals, and for converting the decompressed digital audio signals into analog audio signals;

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means for periodically refreshing the transmitted video signals;

means for running multiple compression and decompression algorithms on all three digital signal processors;

a solid state memory;

means for emulating a disk access system of a computer using solid state memory components to store file sequences with compression/decompression algorithm data; and

a memory for storing a program connected to at least the audio digital signal processor, said memory comprising at least two audio conversion programs for converting audio at first and second respective rates.